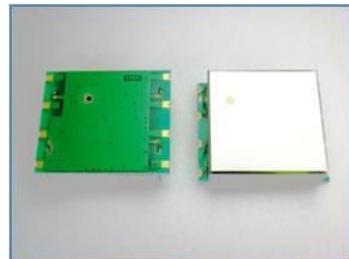


LVPECL VCXO

VXSW-Z7S7 Series

◆特長・用途/Feature

- SMD Package
- Low Phase noise
- Supply Voltage +3.3V
- Output Level LVPECL
- Frequency range 400MHz ~ 500MHz



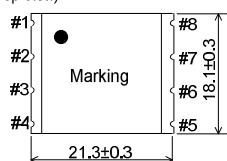
RoHS Compliant

◆仕様/Specifications

項目/ item	Min	Max	単位 / unit	Note
出力周波数 Frequency range	400	500	MHz	--
動作温度範囲 Operating Temp. range	-30	+85	°C	--
電源電圧 Power Supply voltage	+3.3		V	--
消費電流 Current	--	100	mA	--
周波数安定度 Freq. Overall tolerance	±40		10 ⁻⁶	Inclusive of initial tolerance , operating temperature , supply voltage change and aging 1 st year.
周波数可変範囲 Freq. control range	±45	--	10 ⁻⁶	Control Voltage(VC) : +0V ~ +3.3V
出力レベル Output level	VOH	+2.22	--	V
	VOL	--	+1.70	V
Rise time , Fall time	--	2	ns	+1.7V ↔ +2.22V
Duty Cycle	40	60	%	at +1.96V
位相雑音 Phase noise (参考値)	--	-70	dBc/Hz	10Hz Offset
	--	-100	dBc/Hz	100Hz Offset
	--	-120	dBc/Hz	1kHz Offset
	--	-130	dBc/Hz	10kHz Offset
	--	-135	dBc/Hz	100kHz Offset
	--	-140	dBc/Hz	1MHz Offset

◆外形寸法/Dimensions

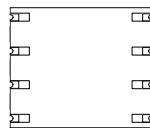
(Top View)



(Side View)



(Bottom View)



Terminal Connections

Terminal#	Connection
1	GND
2	GND
3	GND
4	Vc or NC
5	GND
6	Output
7	Output
8	VDD

Pin#4 is NC for SPXO/TCXO

◆Phase noise DATA

